

Common mode interference of three-phase photovoltaic inverter

How does a power inverter affect the efficiency of a system?

Author to whom correspondence should be addressed. Power inverters produce common mode voltage (CMV) and common mode current (CMC) which cause high-frequency electromagnetic interference (EMI) noise, leakage currents in electrical drives application and grid-connected systems, which consequently drops the efficiency of the system considerably.

Can a voltage source inverter handle a three-phase electric machine?

Nowadays, voltage source inverters (VSIs), along with speed drive controllers, are widely utilised to handle the three-phase electric machines. In all balanced three-phase electric machines with sinusoidal excitations, the summation of phase-to-neutral voltages with Y connection is achieved to be zero in time-domain analysis [1].

What is a non-isolated photovoltaic (PV) system?

In non-isolated photovoltaic (PV) system, the harmful CMV brings about serious leakage current that flows to the ground, which distorts three-phase output currents, causes severe common-mode electromagnetic interference, and even threatens the safe operation of the whole system.

Why does a two-level inverter have high common-mode voltage (CMV)?

With different pulse-width modulation (PWM) methods used, the two-level inverter still suffers from high common-mode voltage (CMV), specifically, the high CMV generated by the partial switch states of the two-level inverter, which lead to common-mode current and electromagnetic interference.

How to reduce distortion in a three-phase three-level inverter?

Conclusions In order to reduce distortion in the system, the modified PWM technique is employed along with EMI chokes. Various PWM strategies are analyzed to reduce the CMV and CMC, and a modified PWM approach is presented for a three-phase three-level inverter.

Can grid-tied power inverters reduce cm voltage and current?

EMI mitigation techniques are investigated with the aim to reduce the CM voltage and current in PV grid-tied power inverters. The common mode undesirable effects for grid-tied inverter systems has been discussed and compared for different PWM schemes.

Therefore, CM noise conduction paths can be summarised in two ways: (i) on the DC side, the CM current flows through the capacitances C_A and C_B to the reference ground and then forms a CM conduction path ...

Leakage current suppression is currently a serious challenge for transformerless grid-tied photovoltaic (PV) inverters. A LCCL-filtered structure can be adopted to mitigate the leakage current, whose filter capacitors are split into two groups, and the neutral point of one group is connected to the negative DC-link. The LCCL filter

can provide a low-impedance CM path to ...

1 College of Electrical and Power Engineering, Taiyuan University of Technology, Taiyuan, China; 2 State Nuclear Power Planning Design and Research Institute CO., Ltd, Beijing, China; In this article, a model predictive control (MPC) with common-mode voltage (CMV) suppression is proposed for single-phase cascaded H-bridge (CHB) inverters, which can also ...

The structure of a three-phase inverter to drive the motor is shown in Fig. 1, where V_D is the direct-current (DC) side voltage, L_f is the inductance of the output filter and C_f is the capacitance of the output filter, respectively. The main reason for its serious CMV is that the three-phase output voltages of the inverter cannot achieve a balance.

In this paper, a three-phase grid-connected photovoltaic (PV) topology (named H8) is proposed to address the leakage current issue. AC common-mode voltage and earth leakage current cause problems ...

This paper proposes methods for common-mode noise prediction for a grid single-phase inverter in a high-frequency model. The high-frequency detailed circuit of the inverter is proposed by measuring or extracting all parasitic impedance of the interconnection devices and the insulated-gate bipolar transistor (IGBT). A mathematical analysis is proposed by applying ...

In the three-phase inverter, the common-mode voltage can be measured between the load neutral point and the general ground. It can be expressed as the mean of the three output voltages measured at the respective phase with respect to common ground. ... common-mode voltages in the inverter worsen electromagnetic interference (EMI). The ...

This paper introduces a new three-phase two-level inverter based on the switched-capacitor voltage multiplier. By adding a voltage multiplier network at the DC side of the traditional three-phase inverter topology, the DC-link voltage of the introduced inverter is stepped up to triple of the input voltage. Compared to the existing solutions, the common-mode voltage ...

DOI: 10.1109/APEMC.2016.7522982 Corpus ID: 41924203; Common mode EMI noise modeling and prediction for a three-phase, three-level, grid tied photovoltaic inverter @article{Zhang2016CommonME, title={Common mode EMI noise modeling and prediction for a three-phase, three-level, grid tied photovoltaic inverter}, author={Huan Zhang and Shuo Wang ...

By summarizing the CM noise models in PV inverter [] and motor drive [] systems, a generalized CM noise model is developed [], as given in Fig. 11.3, where, Z_{CM_in} and Z_{CM_out} are the equivalent CM impedance of the input and output side, respectively. The voltage source is the CM voltage v_{CM} of the inverter that equals to the arithmetic average of ...

Common mode interference of three-phase photovoltaic inverter

Common-mode Voltage In a three-phase AC system, the common-mode voltage can be defined as the voltage difference between the power source ground and the neutral point of a three-phase load. If the load is an AC motor, the neutral point of the load means the stator neutral of the motor. It is important to define the common-mode voltage in ...

Electromagnetic interference (EMI) filters are inevitable parts of power electronic systems. A novel EMI filter for single-phase grid-inverter is proposed in this study, to suppress the common-mode (CM) EMI noise. The noise source and propagation path ...

This study proposes a novel pulse width modulation (PWM) algorithm to mitigate the common mode voltage (CMV) in a multi-level voltage source inverter feeding an electric machine. Dead-time effect frequently ...

3 Phase, 3-Level T-Type Bridge RLISN 3 LISN EUT (a) n Lg 3 Lf 3 3Cf ICM VCM1 O Rd 3 Rg 3 Rf 3 g 3Cp2 3Cp1 Cog 3Cp3 Heat-sink Ground 3 Phase, 3-Level T-Type Bridge RLISN 3 LISN EUT (b) Fig. 4: CM noise models for three level, 3fT-type based grid connected converter (a) proposed CM noise model (b) conventional CM noise model [9], [10].

This paper focuses on the common mode (CM) electromagnetic interference (EMI) problem in a three-phase two-level inverter system. Firstly, the three-phase four-bridge topology is analyzed. The analysis results point out that the output filter midpoint voltage can be used as the observation point of CM voltage. Thus, the actual suppression effect of CM voltage can be ...

In this paper, the CM-EMI of a single-phase PV inverter is first analyzed, and the equivalent circuit of the CM-EMI in the single-phase PV inverter is established, then the CSPWM is employed to ...

Web: <https://www.arcingenieroslaspalmas.es>